| Ref # | Hits | Search Query | DBs | Default Operator | Plurais | Time Stamp |
|-----------|------|---|---|---------------------|---------|------------------|
| L1 | 19 | dram and bit adj line and charge adj storage and switch adj device | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 11:05 |
| L2 | 18 | 1 and read\$3 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 11:00 |
| L3 | 645 | dram and bit adj line and charge adj storage and switch | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 11:05 |
| L4 | 0 | 3 and turn adj on with switch | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR . | ON | 2005/10/18 11:05 |
| L5 | | 3 and turn adj on | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 11:06 |
| L6 | 58 | 3 and turn-on | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 11:12 |
| L7 | 0 | 4 and turn-off | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 11:06 |
| L8 | 2 | 3 and turn-on with switch | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 11:06 |

| L9 | 392 | 3 and read\$3 adj operation | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 11:13 |
|-----|-----|--|---|----|----|------------------|
| L10 | 118 | 9 and read\$3 adj operation same charg\$3 with bit | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 11:13 |
| L11 | 111 | 10 and transistor | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 11:13 |
| L12 | 81 | 10 and transistor with capacitor | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 11:14 |
| L13 | 10 | 6 and 12 | US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/10/18 11:14 |